



BittWare Delivers State-of-the-art FPGA Boards for High Frequency Trading

Altera's Stratix V FPGA with integrated 10GigE and PCIe Gen 2 is ideal for financial applications requiring ultra-low latency

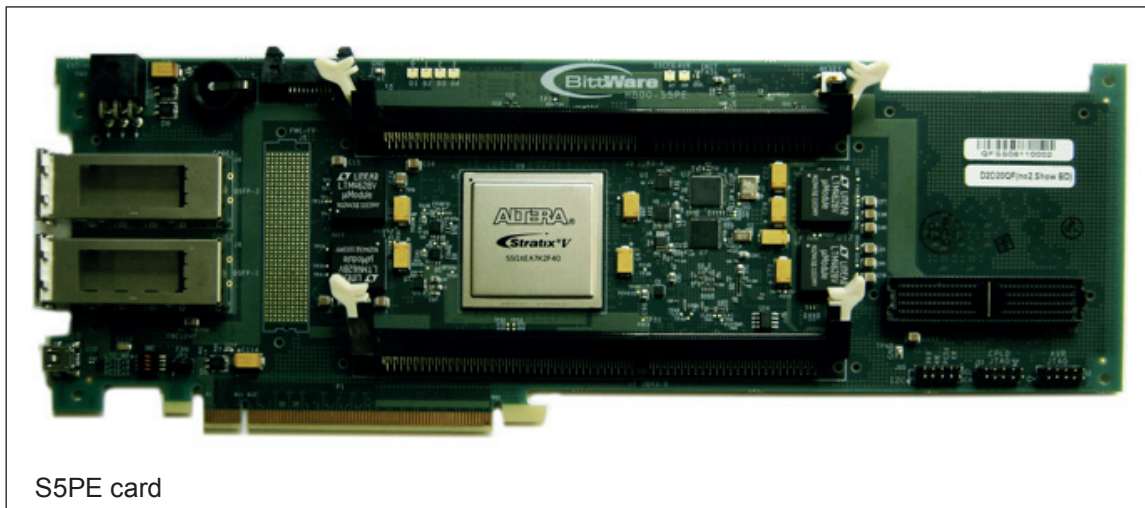
CONCORD, NH - January 17, 2012 - BittWare, a provider of fully-validated, deployable Altera-based off-the-shelf FPGA and signal processing boards, announced today the First Customer Shipment (FCS) of the S5-PCle to a technology leader in the finance and trading industry. The S5-PCle is the first of a family of PCI Express slot-cards that BittWare is offering to the financial acceleration/HFT (High Frequency Trading) market. Providing CPU offload, the highest performance transceivers, and optional TOE (TCP/IP Offload Engine), the S5-PCle is a PCIe-compliant deployable slot-card based on the industry's highest performance FPGA - Altera's Stratix® V. With maximum processing performance per watt to minimize energy and thermal requirements and 10GigE straight to the FPGA (no conversion to XAUI) for ultra-low latency, the S5-PCle is ideal for applications in the high frequency trading market where trades are executed in microseconds, and the ability to minimize the delay between market data arrival and issuing the trade is of the utmost importance.

"In the world of high frequency trading, the lowest latency solution takes the cake," said Darren Taylor, Senior VP of Sales & Marketing at BittWare. "Our S5-PCle fulfills this need from all sides. With multiple lanes of 10GigE straight to the FPGA and PCI Express Gen 2, combined with on-board memory and optional TOE, as well as additional built-in support for 40 GigE and PCIe Gen3, the financial industry has taken notice."

S5-PCle Details

BittWare's S5-PCle (S5PE) is a PCIe x8 card based on the high-bandwidth, power-efficient Altera Stratix V GX or GS FPGA. Designed for high-end applications, the Stratix V provides a high level of system integration and flexibility for I/O, routing, and processing. When combined with BittWare's Anemone™ FPGA co-processor and ATLANTiS™ FrameWork FPGA development kit, the S5PE creates a flexible and efficient solution for high-performance signal processing and data acquisition. The board supports DDR3 SDRAM with error-correcting codes (ECC). Providing additional flexibility are up to two VITA 57 FMC sites for enhancing the board's I/O and processing capabilities. The board also has the option of two front-panel QSFP+ cages for serial I/O.

The S5PE is the first card in BittWare's family of Altera Stratix V PCIe boards. Additional boards offering QSFP, SFP+, QDRII, RLDRAM3 and half-size formats will be introduced and released in late Q1 2012.



S5PE card